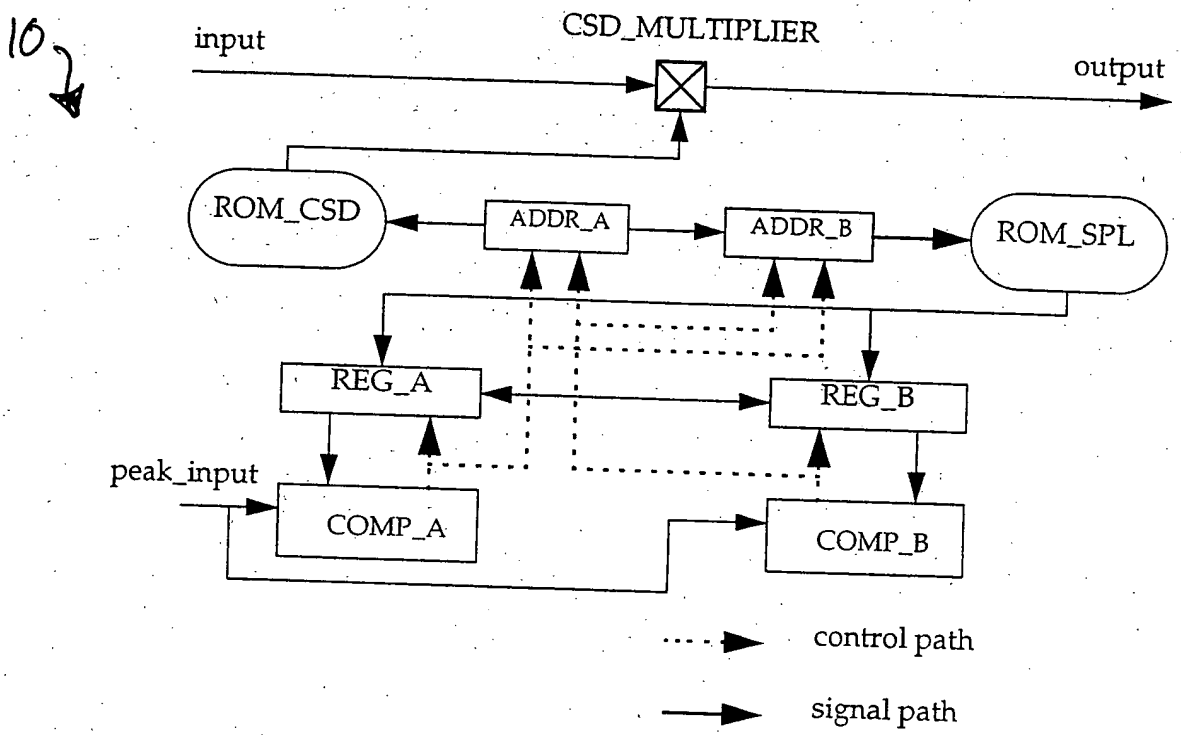

Figure 1. A diagram of the new digital AGC.

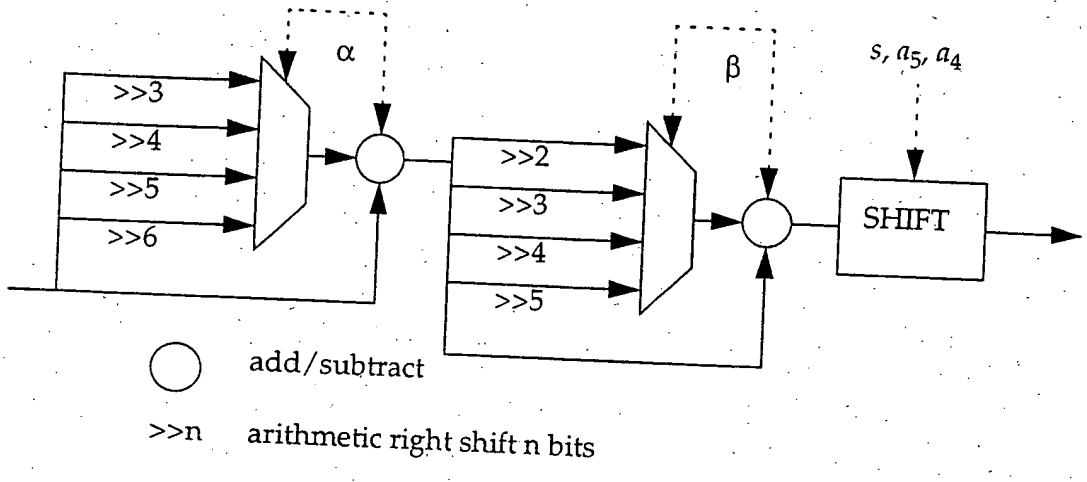

Figure 2. An efficient CSD multiplier using (7).